

Docket No.: 071971-0566

AP20 Rec'd PCT/PTO 10 APR 2006
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Tsuyoshi MATSUSHITA :
Application No.: Not yet assigned : Group Art Unit: Not yet assigned
Filed: April 10, 2006 : Examiner: Not yet assigned
For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT
INCLUDING THE SAME

PRELIMINARY AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments are on page 6 of this paper.